A computer screen shot of a computer

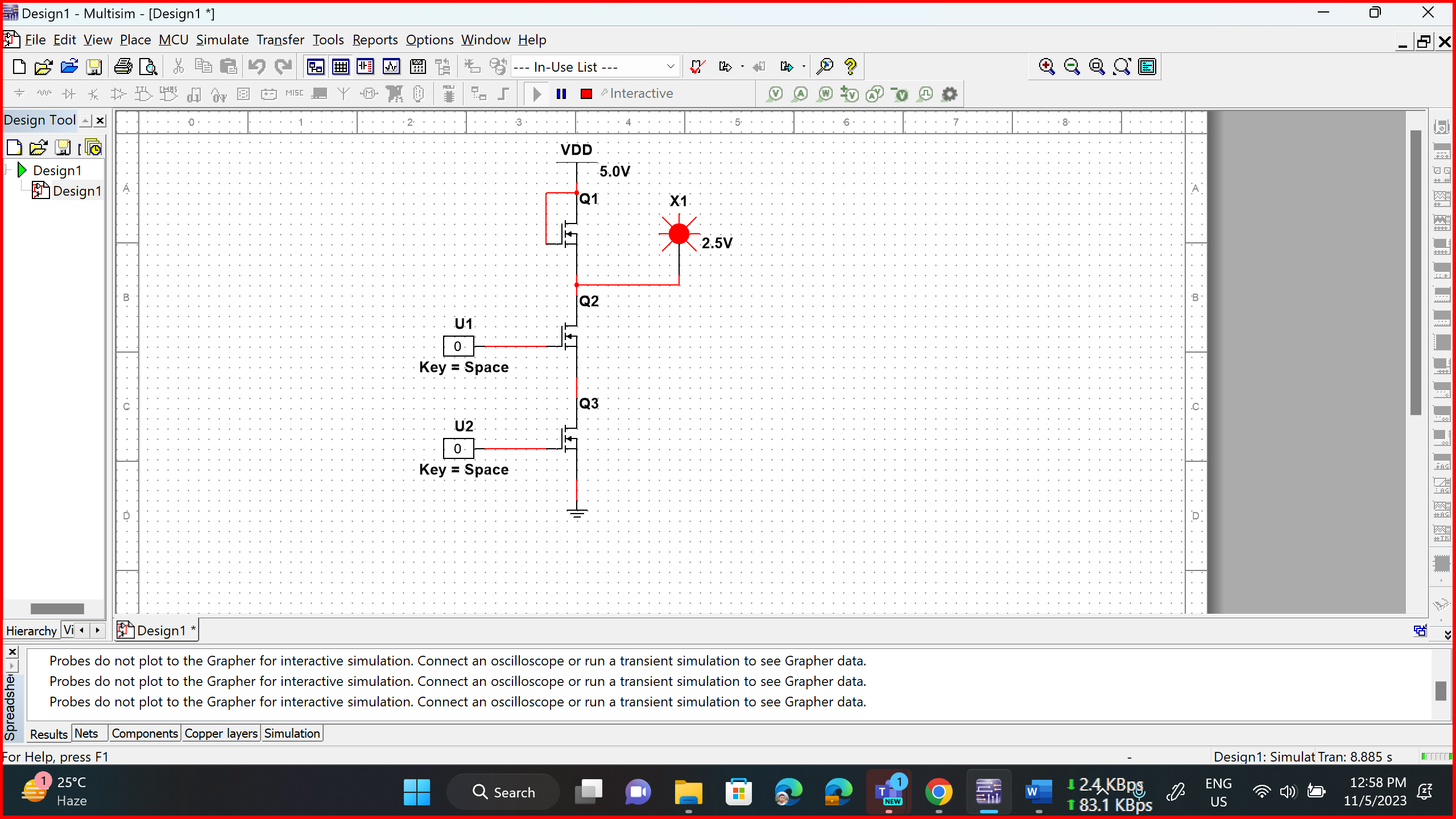
Description automatically generated

NMOS Inverter with NMOS Load

A computer screen shot of a diagram

Description automatically generated

NMOS Inverter with NMOS Load



NMOS NAND Gate

A screenshot of a computer

Description automatically generated

NMOS NAND Gate

A screenshot of a computer

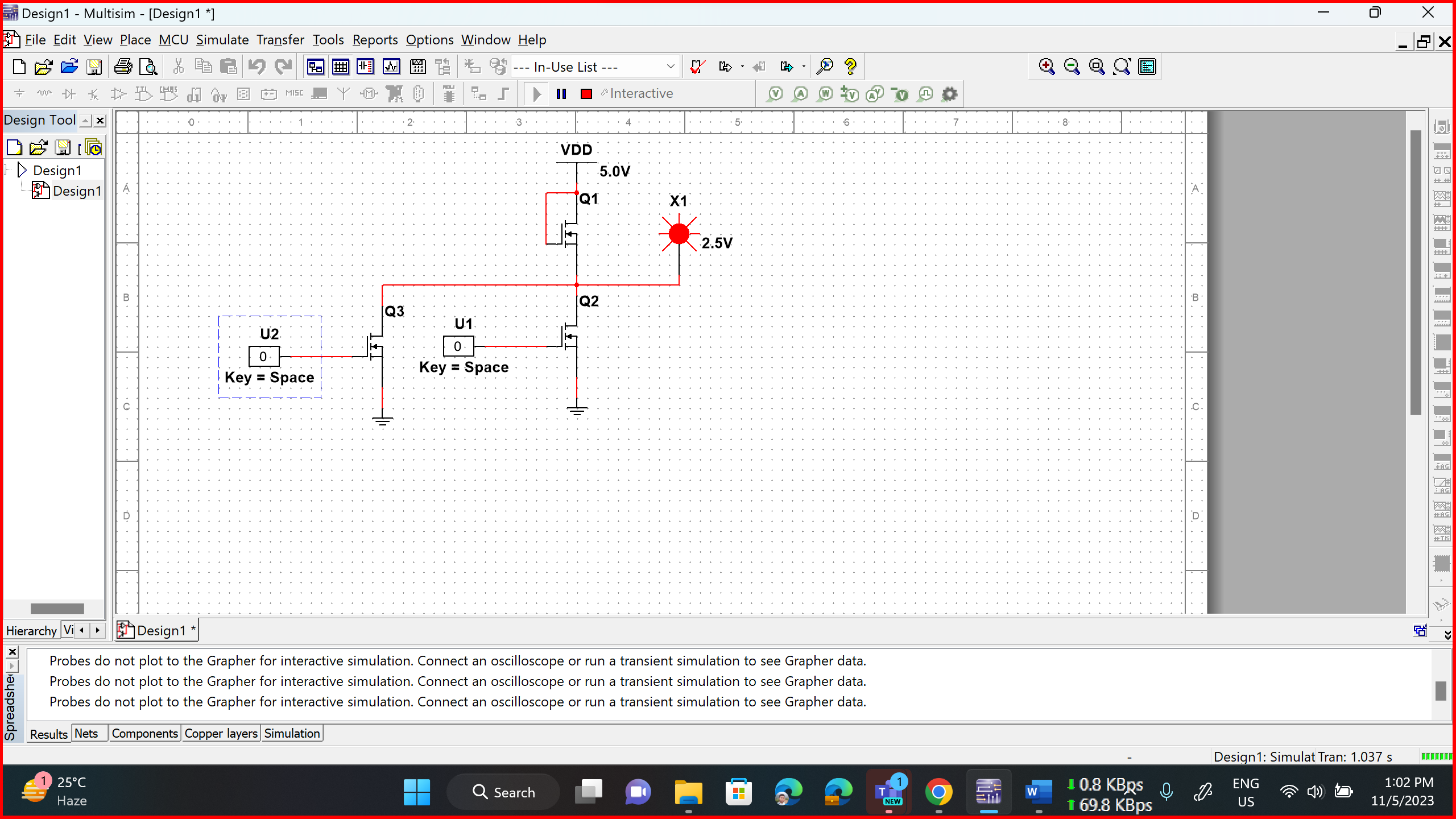
Description automatically generated

NMOS NAND Gate

A screenshot of a computer

Description automatically generated

NMOS NAND Gate



NMOS NOR Gate

A screenshot of a computer

Description automatically generated

NMOS NOR Gate

A screenshot of a computer

Description automatically generated

NMOS NOR Gate

A screenshot of a computer

Description automatically generated

NMOS NOR Gate